



NOTICE

M.S.BIDVE ENGINEERING COLLEGE, LATUR

Department of Electronics Engineering

Date: 09/03/2019

All the students of M.Tec. (E&C) are hereby informed to note the time table of first unit test for part II (Ac. Yr. 2018-19)

Day / Date	Time	Subject
THU 14/03/19	11 TO 12	I & VP
THU 14/03/19	3:00 TO 4:00	I T & C
FRI 15/03/19	11 TO 12	ELE-III
FRI 15/03/19	3:00 TO 4:00	ELE-IV
SAT 16/03/19	11 TO 12	ELE-V

Az
9/3/19
Co-ordinator

Yenle
HOD



**DR. BABASAHEB AMBEDKAR TECHNOLOGICAL UNIVERSITY, LONERE-
RAIGAD- 402 103**

Mid Semester Examination- March- 2019

Branch: M.Tech (E&C)

Sem.: -II

Subject with Subject Code :-) Image and video Processing (MTECC201)

Marks:-20

Date:-14/3/2019

Time: - 1Hr.

Instruction:-

(Marks)

Q.No.1 Attempt any one of the following (08)

- a) Consider 3 bit 64×64 image. Its intensity distribution is as shown in Table. Equalize Histogram and draw it.

R0	R1	R2	R3	R4	R5	R6	R7
790	1023	850	656	329	245	122	81

- b) Give a short note on Adaptive filters
c) What is redundancy? Explain with example.

Q.No.2 Attempt any three of the following (12)

- a) Explain any two noise probability density functions.
b) What is piecewise linear transformation? Explain with example.
c) Explain basic intensity transformation function in spatial filtering.
d) Compute lengths of shortest 4, 8, m path between p and q if $v = \{1, 2\}$

3 1 2 1(q)

2 2 0 2

1 2 1 1

(P) 1 0 1 2

- e) What is lossless compression? Explain with one example.

Notice

Dr. Babasahab Ambedkar Technological university,
M. S. Bidve Engineering College, Latur
Department of Electronics Engineering
AY 2019-2020 part – I Class Test: CA II

Date:- 08/09/2019

Date	Time	SE	Subject	TE	Subject
11/11/2019	10 am. To 10.40 am.	AC		CS	
13/11/2019	10 am. To 10.40 am.	NA		DSCA	
14/11/2019	10 am. To 10.40 am.	DLD		ME	
15/11/2019	10 am. To 10.40 am.	EDC		MPI	
16/11/2019	10 am. To 10.40 am.	MIII		DSP	
18/11/2019	10 am. To 10.40 am.	---		EMFT	


HOD
08/11/19

M.S.BIDVE ENGG COLLEGE
UNIT TEST II

SUB: DVLSI

CLASS: BE (EC)

TIME: 1Hr

DATE: 1/11/2018

NOTE: Solve Any 4 each question carry equal marks. (5*4Q=20)

- | | |
|---|------------------|
| Q. 1.Design 4 bit ripple carry static adder. | 5M |
| Q. 2.Give a short note on 4*4 OR ROM cell array. | 5M |
| Q. 3.Design 4*4 Array multiplier | 5M |
| Q. 4.give short note on Barrel shifter. | 5M |
| Q. 5. Design 1 bit adder using transmission gate | 5M |
| Q. 6.realise a regular PLA for the following Equation | |
| $Y1=AB+BC+AC$ | $Y2=ABC+ABC+ABC$ |
| $Y3=DE+DE$ | $Y4=DEF+DEF$ |

M.S.BIDVE ENGG COLLEGE
UNIT TEST II

SUB: DVLSI

CLASS: BE (EC)

TIME: 1Hr

DATE: 1/11/2018

NOTE: Solve Any 4 each question carry equal marks. (5*4Q=20)

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M.S.BIDVE ENGG COLLEGE
UNIT TEST II

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M. S. Bidve Engineering College, Latur

Classwise Academic Audit

Department:- Electronics Class:- TY B.C. Ac. Year: 2022-23 (Result Summer-23)

SEM: VI

Semester: Even

Sr. No.	Code	Subject (Theory)	Name of Faculty	No of Stud.	Pass	Fail	%	Sign.
1	BTEXC 601	POWER ELECTRONICS	Prof. Bale V. S.	63	60	3	95.24	
2	BTEXC 602	MICROPROCESSOR & MICROCONTROLLER	Prof. Mudda S. S.	63	52	11	82.54	
3	BTEXC 602	MICROPROCESSOR & MICROCONTROLLER	Prof. Khanapure R. P.	63	52	11	82.54	
4	BTECP603	CONTROL SYSTEM E.	Prof. Mudda S. S.	63	58	5	92.06	
5	BTEXOE604	COMMUNICATION Engg.	Dr. S. R. Halhalli	63	56	7	88.89	
6	BTHM605	EMPLOYABILITY & SKILL DEV.	Prof. Solapurkar U. B.	63	61	2	96.83	

Class Mentor

HOD

Department:- Electronics

Class:- SY EC.

Ac. Year: 2022-23 (Result Summer-23)

SEM: IV

Semester: Even

Sr. No.	Code	Subject (Theory)	Name of Faculty	No of Stud.	Pass	Fail	%	Sign.
1	BTES401	ELECTRICAL M/C & INSTRU.	Prof. Khanapure R. P.	64	57	8	89.06	
2	BTEXC402	SIGNAL & SYSTEM	Prof. Bale V. S.	64	30	34	46.88	
3	BTHM403	BASIC HUMAN RIGHTS	Prof. S. S. Shetkar	64	64	0	100.00	
4	BTHM403	BASIC HUMAN RIGHTS	Dr. S. R. Halhalli	64	64	0	100.00	
5	BTBS404	PROB. THEORY & RANDOM PROSS.	Prof. J. S. Hätte	64	63	1	98.44	
6	BTEXPE405	COMPUTER ORG. & ARCT.	Prof. V. K. Shah	64	47	17	73.44	

Class Mentor

HOD

Department:- Electronics

Class:- FINAL YEAR.

Ac. Year: 2022-23 (Result Summer-23)

SEM: VIII

Semester: Even

Sr. No.	Code	Subject (Theory)	Name of Faculty	No of Stud.	Pass	Fail	%	Sign.
1	BTEPPE801A	INTRO. TO INTERNET OF THINGS	Prof. S. S. Killariker	36	33	3	91.67	
2	BTEPPE802F	DIGITAL IC DESIGN	Prof. S. S. Shetkar	36	27	9	75.00	

Classwise Academic Audit

Class:- SY EC.

Ac. Year: 2022-23 (Result Winter-22)

SEM: III

Semester: Odd

St. No.	Code	Subject (Theory)	Name of Faculty	No of Stud.	Pass	Fail	%	Sign.
1	BTBS301	Engineering Math-III	Prof. G. Shabade	72	31	41	43.00	<i>[Signature]</i>
2	BTEXC302	Electronics Devices & Circuits	Prof. V. K. Shah	72	54	18	75.00	<i>[Signature]</i>
3	BTEXC303	Digital Electronics	Dr. S. R. Halhali	72	53	19	73.61	<i>[Signature]</i>
4	BTEXC304	Network theory	Prof. J. S. Hatte	72	41	31	56.94	<i>[Signature]</i>

Class Mentor

HOD

Department:- Electronics

Class:- TY EC.

Ac. Year: 2022-23 (Result Winter-22)

SEM: V

Semester: Odd

St. No.	Code	Subject (Theory)	Name of Faculty	No of Stud.	Pass	Fail	%	Sign.
1	BTEXC 501	Analog Circuits	Prof. Bale V. S.	62	52	10	83.87	<i>[Signature]</i>
2	BTEXC 502	Digital Signal Processing	Prof. Solapurkar U. B.	62	60	2	96.77	<i>[Signature]</i>
3	BTEXC 503	Microelectronics	Prof. K. Karikar S. S.	62	45	17	72.58	<i>[Signature]</i>
4	BTEXPE504	Electromagnetic Field Theory	Prof. V. K. Shah	62	43	19	69.35	<i>[Signature]</i>
5	BTEXOE505	Digital System Design	Prof. Mudda S. S.	62	45	17	72.58	<i>[Signature]</i>

Class Mentor

HOD

Department:- Electronics

Class:- FINAL YEAR.

Ac. Year: 2022-23 (Result Winter-22)

SEM: VII

Semester: Odd

St. No.	Code	Subject (Theory)	Name of Faculty	No of Stud.	Pass	Fail	%	Sign.
1	BTEXC701	Antenna Wave Propagation	Prof. Mudda S. S.	36	30	6	83.33	<i>[Signature]</i>
2	BTEXPE702	Digital Image Processing	Prof. S. S. Shetkar	36	33	3	91.67	<i>[Signature]</i>
3	BTEXPE703	CMOS	Prof. S. S. Shetkar	36	32	4	88.89	<i>[Signature]</i>
4	BTEXPE704	Wireless Communication	Prof. Khanapure R. P.	36	35	1	97.22	<i>[Signature]</i>
5	BTM705	Financial Management	Prof. Sudke R. O.	36	36	0	100.00	<i>[Signature]</i>

Class Mentor

HOD

DR. BABASAHEB AMBEDKAR TECHNOLOGICAL UNIVERSITY, LONERE Contentious Assessment Test-I – Oct 2019 Course: B. Tech in Electronics Engineering Subject Name: Digital Logic Design Max Marks:10 Date:-23/10/2019 Time:10.45 to 11.45 AM Duration:- 1 Hr.			
Instructions to the Students: 1. All questions are compulsory. 2. Figures to the right indicates full marks. 3. Assume suitable data where ever necessary.			
		(Level/CO)	Marks
Q.1	Solve Any One of the Following.		5 X 1
(A)	Convert D-FF to T-FF.	Creating	
(B)	Design 3 bit SIPO shift register.	Applying	
Q.2	Solve Any One of the following.		5 X 1
(A)	Design Mod 9 ripple counter.	Applying	
(B)	Design synchronous counter to count the sequence $0 \rightarrow 2 \rightarrow 4 \rightarrow 7 \rightarrow 0$.	Applying	

DR. BABASAHEB AMBEDKAR TECHNOLOGICAL UNIVERSITY, LONERE Contentious Assessment Test-I – Oct 2019 Course: B. Tech in Electronics Engineering Subject Name: Digital Logic Design Max Marks:10 Date:-23/10/2019 Time:10.45 to 11.45 AM Duration:- 1 Hr.			
Instructions to the Students: 4. All questions are compulsory. 5. Figures to the right indicates full marks. 6. Assume suitable data where ever necessary.			
		(Level/CO)	Marks
Q.1	Solve Any One of the Following.		5 X 1
(A)	Convert D-FF to T-FF.	Creating	
(B)	Design 3 bit SIPO shift register.	Applying	
Q.2	Solve Any One of the following.		5 X 1
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(B)	Design synchronous counter to count the sequence $0 \rightarrow 2 \rightarrow 4 \rightarrow 7 \rightarrow 0$.	Applying	

DR. BABASAHEB AMBEDKAR TECHNOLOGICAL UNIVERSITY, LONERE

Mid Semester Examination – Oct 2019

Course: B. Tech in Electronics Engineering

Semester: III

Subject Name: Digital Logic Design

Subject Code: BTEXC305

Max Marks:20

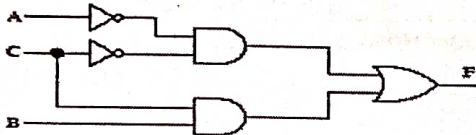
Date:-03/10/2019

Time:10.30 to 11.30 AM

Duration:- 1 Hr.

Instructions to the Students:

1. All questions are compulsory.
2. Figures to the right indicates full marks.
3. Assume suitable data where ever necessary.

		(Level/CO)	Marks
Q.1	Multiple choice questions	Understand	6
	1. De-Mux is a _____ circuit (i) Combinational (ii) Sequential (iii) Feed back		
	2. Each individual term in a standard POS form is called _____ (i) Literal (ii) Min Term (iii) Max Term		
	3. The min term expansion for $F(A,B,C)$ in the diagram below is:  (i) $ABC + \bar{A}\bar{B}C + A\bar{B}\bar{C} + \bar{A}\bar{B}\bar{C}$ (ii) $\bar{A}\bar{C} + BC$ (iii) $\bar{A}B + BC$ (iv) $\bar{B} + AC$		
	4. In a four variable K-Map, half the cells are filled with logic 1 and remaining half with don't care for an output function $f(ABCD)$. What is the simplified expression? (i) 0 (ii) 1 (iii) X (iv) $A + B + C + D$		
	5. In a comparator if the cascade input $A=B$ is connected to logic 1, $A>B$ and $A<B$ to don't care, then output $A>B$ will be; (i) 1 (ii) 0 (iii) x (iv) None of these		
	6. An OR gate has 4 inputs. One input is high and the other three are low. The output is (i) Low (ii) High (iii) Alternate High and Low (iv) may be high or low depending on relative magnitude of inputs		

Q.2	Solve Any Two of the following.	Evaluate	
(A)	Simplify the following function using K-Map $F(ABCD) = \sum m(0,1,2,3,5,7,8,14,15) + \sum d(11,13)$		
(B)	Realize Full subtractor using MUX		
(C)	Show using 4 bit adder (IC 7483), how subtraction can be carried out using 2's complement		
Q.3	Solve Any One of the following.		
(A)	Design Binary to gray and gray to binary code converter.	Apply	8
(B)	Design the logic circuit of 4 line to 10 line i.e. BCD to Decimal decoder using logic gates.		
	*** End ***		

DR. BABASAHEB AMBEDKAR TECHNOLOGICAL UNIVERSITY, LONERE Contentious Assessment Test-II – Nov. 2019 Course: B. Tech in Electronics Engineering Subject Name: Digital Logic Design Max Marks: 10 Date:-14/11/2019 Time: 10.00 to 10.30 AM Duration:- 30. Min.				
Instructions to the Students: 1. Solve any two of the following. 2. Figures to the right indicates full marks. 3. Assume suitable data where ever necessary.				
			(Level/CO)	Marks
Q. 1	Explain with necessary block diagram Moore and Mealy machine.		Creating	5
Q.2	Derive the state table, state diagram and state equations for the sequential circuit shown (Mealy Machine).		Applying	5
Q.3	Draw the state diagram and state table for the sequence detector to detect the sequence 1011 (Use Moore Machine).		Applying	5
Q.4	Design a synchronous counter for the sequence 2 → 3 → 1 → 6 → 7...2. avoid lockout condition (use T FF).		Applying	5

DR. BABASAHEB AMBEDKAR TECHNOLOGICAL UNIVERSITY, LONERE Contentious Assessment Test-II – Nov. 2019 Course: B. Tech in Electronics Engineering Subject Name: Digital Logic Design Max Marks: 10 Date:-14/11/2019 Time: 10.00 to 10.30 AM Duration:- 30. Min.				
Instructions to the Students: 1. Solve any two of the following. 2. Figures to the right indicates full marks. 3. Assume suitable data where ever necessary.				
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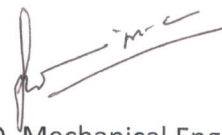


M.S. Bidve Engineering College, Latur
Mechanical Engg. Dept.
Notice

21-22
11.04.2022

All the students of B.Tech. S.Y.,T.Y,(Mechanical). (DBATU,Lonere) are hereby informed that their **(CA-I) Test, (Offline)** will be conducted as per following schedule.

Date	Time	S.Y.	T.Y
22/04/2022	11.30 am TO 12.00 noon	Manufacturing Processes - I	Manufacturing Processes- II
	2.30 pm TO 3.00 pm	Theory of Machines-I	IC Engines
23/04/2022	11.30 am TO 12.00 noon	Strength of Materials	Applied Thermodynamics- II
	2.30 pm TO 3.00 pm	Numerical Methods in Engineering	Machine Design-II
	4.30 pm TO 5.00 pm	Basic Human Rights	Quantitative Techniques in Project Management


HOD, Mechanical Engg. Dept.



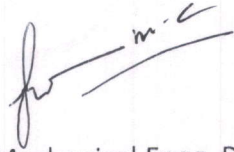
21-12

M.S. Bidve Engineering College, Latur
Mechanical Engg. Dept.
Notice

12.04.2022

All the students of B.Tech. Final Year (Mechanical). (DBATU, Lonere) are hereby informed that their **(CA-I) Test**, Online mode, (Google form) will be conducted as per following schedule.

Date	Time	Final Year
22/04/2022	1.30 pm TO 2.00 pm	FAS
23/04/2022	1.30 pm TO 2.00 pm	NCER


HOD, Mechanical Engg. Dept.



INTERNAL TEST ATTENDANCE

DEPARTMENT OF ELECTRONICS ENGINEERING

AC YEAR : 2022-23 SEMESTER : Odd/ Even CLASS : T.Y. SUBJECT : DSD

Invigilator Signature

FACULTY : Mudda. S. S.

Exam. Date	08/10/22	10/11/22	24/12/23
Name of the Student	CA-1	MID Exam.	CA-2
ADITYA PRATAP SHIRURE	AA	AB	AB
ARBAB NAWABODDIN BAGWAN	ABBAZ	ABBAZ	ABBAZ
AMAN JUBER KURESHI	ABBAZ	ABBAZ	ABBAZ
ANURAG POPAT BAJPAI	ABBAZ	ABBAZ	ABBAZ
NANDINI SANJIV KUMDALE	ABBAZ	ABBAZ	ABBAZ
NITIN RAMESHRAO JADHAV	ABBAZ	ABBAZ	ABBAZ
PRASAD BALKRISHNA VAJWADE	ABBAZ	ABBAZ	ABBAZ
RUTUJA SHRIRAM MARE	ABBAZ	ABBAZ	ABBAZ
SUSHANT SATISH CHAVAN	ABBAZ	ABBAZ	ABBAZ
SAVYASACHI DHANANJAY KULKARNI	ABBAZ	ABBAZ	ABBAZ
SURYAWANSHI SRUSHTI	SRUSHTI	SRUSHTI	SRUSHTI
SUBHASHRAO	AA	-	SNEHA
KHARATE SNEHA JYOTIRAM	ABBAZ	ABBAZ	ABBAZ
GANGTHADE SHUBHANGI	ABBAZ	ABBAZ	ABBAZ
BALAJIRAO	ABBAZ	ABBAZ	ABBAZ
MATOLE SHITAL SAHADEV	ABBAZ	ABBAZ	ABBAZ
GOSAVI AMEYA SURYAKANT	ABBAZ	ABBAZ	ABBAZ
JAGTAP ROHAN PANJAB	ABBAZ	ABBAZ	ABBAZ
DHANORKAR MAITHILI MANIKRAO	ABBAZ	ABBAZ	ABBAZ
CHOUDHARI SIDDHESHVAR	ABBAZ	ABBAZ	ABBAZ
BHALCHANDRA	ABBAZ	ABBAZ	ABBAZ
SAVALSURE ABHISHEK ASHOK	ABBAZ	ABBAZ	ABBAZ
SAVALSURE ABHISHEK ASHOK	ABBAZ	ABBAZ	ABBAZ



Maharashtra Education Society's
M. S. BIDE ENGINEERING COLLEGE,
 (Approved by AICTE, New Delhi & DTE, Mumbai, Affiliated to P. J. Somaiya Institute of Technology, Lonerare)
 P.O.Box No. 112, Barchi Road, LATUR-413 531 (Maharashtra)



MAAC Accredited

DTE Code : EN2129

INTERNAL TEST ATTENDANCE

DEPARTMENT OF ELECTRONICS ENGINEERING

MIC YEAR : 2022-23 SEMESTER : Odd/Even CLASS : TY SUBJECT : DSD FACULTY : Mudda. S. S.

	Invigilator Signature			
	Exam. Date			
Roll No.	Name of the Student	CA-1	MID Exam.	CA-2
	ADITYA PRATAP SHIRURE	AA	AB	AB
	ARBAB NAWABODDIN BAGWAN	ABba2	ABba2	ABba2
	AMAN JUBER KURESHI	AB	AB	AB
	ANURAG POPAT BAUPAI	AB	AB	AB
	NANDINI SANJIV KUMDALE	AB	AB	AB
	NITIN RAMESHRAO JADHAV	AB	AB	AB
	PRASAD BALKRISHNA VALUWADE	AB	AB	AB
	RUTUJA SHIRIRAM MARE	AB	AB	AB
	SUSHANT SATISH CHAVAN	AB	AB	AB
	SAVYASACHI DHANANJAY KULKARNI	AB	AB	AB
	SURYAWANSHI SRUSHTI	AB	AB	AB
	SUBHASHRAO	AA	-	AB
	KHARATE SNEHA JYOTIRAM	AB	AB	AB
	GANGTHADE SHUBHANGI	AB	AB	AB
	BALAJIRAO	AB	AB	AB
	MATOLE SHITAL SAHADEV	AB	AB	AB
	GOSAVI AMEYA SURYAKANT	AB	AB	AB
	JAGTAP ROHAN PANJAB	AB	AB	AB
	DHANORKAR MAITHILI MANIKRAO	AB	AB	AB
	CHOUDHARI SIDDHESHWAR	AB	AB	AB
	BHALCHANDRA	AB	AB	AB
	SAVALSURE ABHISHEK ASHOK	AB	AB	AB
	SHUBBANGI SHARAD INGLE	AB	AB	AB

1	BIDVE RAJSHRI DIPAK	Prajshel	Prajshel	Prajshel
2	PATIL LAXMI RAJKUMAR	RA		
3	PATIL SAKSHI VIVEK	Patil	AA	Patil
4	VHANALE AARTI VILAS	Bhavana	Bhavana	Bhavana
5	DESHMUKH SANTOSH	Santosh	Santosh	Santosh
6	HARISHCHANDRA	shashank	shashank	shashank
7	MATHPATI SHASHANK NANDKUMAR	Sakshi	Sakshi	Sakshi
8	KAMBLE SAKSHI DAGDU	Sakshi	Sakshi	Sakshi
9	AINILE TANUJA DAYANAND	Atanya	Atanya	Atanya
10	DYANESHWAR KERBA MANE	Mane	Mane	Mane
11	BHANGE AMRUTA SURESH	Amruta	Amruta	Amruta
12	PANCHAL YOGESH MADHAV	Yogesh	Yogesh	Yogesh
13	SURYAWANSHI SONALI DHONDIRA	Sonal	sonal	Sonal
14	PATHAN TAMKEEN FATIMA MABUD	Tamara	fatima	Tamara
15	NANGARALE ANJALI RAMRATAN	Anjali	Anjali	Anjali
16	PATHAN NOMANKHAN ASIF	Noman	Noman	Noman
17	KAMBLE SWATI RAMCHANDRA	swati	swati	swati
18	PUJARI NIKHIL DNYANOBA	Nikhil	Nikhil	swati
19	GANESH ASHOK PATIL	ganesh	ganesh	ganesh
20	PATEL MUSKAN AZIZ	Muskan	Muskan	Muskan
21	MANE SUCHITA PARSHURAM	Suchita	Suchita	Suchita
22	PANCHAL SHREYA JAGANNATH	Panchal	Panchal	Panchal
23	SWAMI VAISHNAVI VIVEKANAND	AA		Panchal
24	SURYAWANSHI SAYALI GOVIND	Sayali	Sayali	Sayali
25	DAIWADNYA AKANKSHA	Akanksha	Akanksha	Akanksha
26	SHASHIKANT			
27	GIRI ANKITA DHANRAJ	AA	ANKITA	ANKITA
28	BORADE ANIKET RAJESHAHEB	ANIKET	ANIKET	ANIKET

48	SAYYED SHIFA IMDADULLA HUSSAINI	<u>Shifa</u>	<u>Shifa</u>	<u>Shifa</u>
49	PHAD ROHIT MAHADEV	<u>Rohit</u>	<u>Rohit</u>	<u>Rohit</u>
50	SOLANKE MAHESH KHANDU	<u>Solanke</u>	<u>Solanke</u>	<u>Solanke</u>
51	ZIRMIRE VAISHNAVI MURLIDHAR	<u>Vaishnavi</u>	<u>Vaishnavi</u>	<u>Vaishnavi</u>
52	DHANURE YADNYANG UDAY	<u>Yadnyang</u>	<u>Yadnyang</u>	<u>Yadnyang</u>
53	DABRE ROHINI RAJKUMAR	<u>Rohini</u>	<u>Rohini</u>	<u>Rohini</u>
54	AKASH BALASAHEB JADHAV	<u>AA</u>		
55	HASURE SANYUKTA SHRIKANT	<u>Sanyukta</u>	<u>Sanyukta</u>	<u>Sanyukta</u>
56	NADGIRE SHIVANI SANTOSH	<u>Shivani</u>	<u>Shivani</u>	<u>Shivani</u>
57	GOJAMGUNDE SHARAYU SHALIK	<u>Sharayu</u>	<u>Sharayu</u>	<u>Sharayu</u>
58	ALTE MAYURI DATTATRAYA	<u>Mayuri</u>	<u>Mayuri</u>	<u>Mayuri</u>
59	KUMBHEJKAR AKSHATA SHRINIVAS	<u>Akshata</u>	<u>Akshata</u>	<u>Akshata</u>
60	GHATANI ASHWINI DHANRAJ	<u>Ashwini</u>	<u>Ashwini</u>	<u>Ashwini</u>
61	MOHINI BALASAHEB YADAV PATIL	<u>Mohini</u>		<u>Mohini</u>
62	KHANDELWAL SHUBHAM RAMESH	<u>Shubham</u>	<u>Shubham</u>	<u>Shubham</u>
63	MUGALE KRISHNA ANIL	<u>Krishna</u>	<u>Krishna</u>	<u>Krishna</u>
64	PATIL AKASH ASHOK	<u>Akash</u>	<u>Akash</u>	<u>Akash</u>
65	SHAikh AMAN HANIF	<u>Aman</u>	<u>Aman</u>	<u>Aman</u>
66	SWAMI SHIVANI MAHADEV	<u>Shivani</u>	<u>Shivani</u>	<u>Shivani</u>
67	VADDEPALLI GAYATRI AMBADAS	<u>Gayatri</u>	<u>Gayatri</u>	<u>Gayatri</u>

H.O.D

Electronics Department

M.S. Bidve Engineering College

LATUR - 413 512



CA1. MC MTech(ECE), DBATUL

Questions Responses 4

Total points: 12

4 responses



Accepting responses



Summary

Question

Individual

tejashwinisavalge1998@gmail.com



1

of 4



8 of 12 points

Score released Aug 13 6:23 PM

Release score

DBATUL, CA-1 TEST, MC EI-V (Open to All) (MTECE255), MTech(ECE), Sem.-II, AY: 2020-21

DR. BABASAHEB AMBEDKAR TECHNOLOGICAL UNIVERSITY, LONERE

M. S. Bidve Engineering College, latur

Department of Electronics Engineering

Course: M. Tech in Electronics & Communication Engineering, Class: MTech (ECE), Part-II, A.Y: 2020-21,

Subject: Multimedia Communication Elective-V (Open to All) (MTECE255)

Max Marks:10 Date:-13/08/2021 Time: 04.30 to 04.50 PM Duration:- 20 Minutes.

Note:

1. Attempt any 10 MCQ's out of 12,
2. Each question has a weightage of one mark,
3. Submit only one response,
4. There is no negative marking,
5. Submit the response form before the end of exam session time
6. It is mandatory to fill the details as requested in the star marked cells before submitting the Response form otherwise it will not be accepted.

* Required

Email *

tejashwinisavalge1998@gmail.com

0 of 0 points

Full name of the student (Last name, Middle name, first name) *

/ 0

Savalge Tejashwini savalge

Add individual feedback

Mobile Number (WhatsApp) *

/ 0

7083647679

Add individual feedback

Class Roll No. *

/ 0

3

Add individual feedback

PRN *

/ 0

3

Add individual feedback

Untitled Section

8 of 12 points

✓ _____ is the characteristic of sound quality that enables a listener to distinguish one kind of musical instrument from other kinds.

1 / 1

- ☐ Pitch
- ☒ Timber
- ☐ Polyphony
- ☐ All of the above



Add individual feedback

✓ Which of the following is the last stage of production?

1 / 1

- ☐ Testing
- ☐ Marketing
- ☒ Distribution
- ☐ Conceptualizing



Add individual feedback

✓ High data compression in audio/video is associated with:

- ☒ Poor reconstruction quality.
- ☐ Increased processing time.
- ☐ Increased transmission time.
- ☐ Increased channel error rate.

Add individual feedback

✓ _____ is used to compress video

- ☒ MPEG
- ☐ JPEG
- ☐ Both MPEG and JPEG
- ☐ None of the above

Add individual feedback

✓ _____ is used to compress Images

- ☐ MPEG
- ☒ JPEG
- ☐ Both MPEG and JPEG
- ☐ None of the above

Add individual feedback

✓ MPEG is _____ compression

1 / 1

- ☒ Lossy ✓
- ☐ Lossless
- ☐ Both Lossy and Lossless
- ☐ May be Lossy or Lossless

Add individual feedback

✗ Which among the following is not an Authoring Tool?

0 / 1

- ☐ CD Drive
- ☐ Macromedia Flash
- ☐ Adobe Director
- ☒ Quest ✗

Correct answer

- ☒ CD Drive

Add individual feedback

✓ Which among the following is not a Digital Audio File Format?

1

- ☐ WAV
- ☐ AU
- ☐ AIFF
- ☒ SNDE

Add individual feedback

✗ What is the other name for interactive multimedia?

0

100%

- ☐ Hypertext
- ☒ Graphic media
- ☐ Hypermedia
- ☐ Print media

✗

Correct answer

- ☒ Hypermedia

Add individual feedback

✓ Ability to play number of notes at once is called as _____

1 / 1

- ☒ Polyphony
- ☐ Multiphony
- ☐ synthesis
- ☐ none of the above



Add individual feedback

✗ Which among the following is not true for MIDI?

0 / 1

- ☐ With MIDI, it is difficult to playback spoken dialog, while digitized audio can do so with ease.
- ☐ MIDI does not have consistent playback quality while digital audio provides consistent audio quality.
- ☒ MIDI files sound better than digital audio files when played on a high quality MIDI device.
- ☐ MIDI data is not editable



Correct answer

- ☒ MIDI data is not editable

Add individual feedback

✕ Which among the following is not an audio file format group? In the _____ transmission mode, communication is unidirectional. 0 / 1

- ☐ Uncompressed
- ☐ Lossless compressed
- ☒ Lossy compressed
- ☐ Noisy compressed

Correct answer

- ☒ Noisy compressed

Add individual feedback

Submitted 8/13/21, 6:18 PM

Dr. Babasaheb Ambedkar Technological University



Title

Student Marks

Exam Season

Winter Semester Examinations 2021

2129 / M.S. Bidve Engineering College, Latur

Course

11246 / 2017 / Bachelor of Technology (Information Technology)

Subject

BTITC305 / 2021 / Data Structures and Applications

Exported On

2022/07/06 14:27:09

Enrollment Number	Full Name	Continuous Assessment 2 Marks / 10	Continuous Assessment 1 Marks / 10	Mid Exam Marks / 20	End Exam Marks / 60
		Template Upload	Template Upload	Template Upload	
2121291246501	SHINDE AKSHTA ANIL	5	7	14	
2121291246502	TANMAY SUDHIR PATIL	4	6	13	
2121291246503	BHALEKAR DIKSHA PANDIT	4	5	11	
2121291246504	PAWAR KAJAL JAYHIND	8	9	15	
2121291246505	KHARAT NIVRATTI RAMESH	4	4	8	
2121291246506	MENGAL PRATIKSHA LAXMIKANT	4	7	15	
2121291246507	PAWAR SHRAVANI BALAJI	6	7	14	
2121291246508	BANSODE SIDDHARTH SHRIDHAR	4	5	16	
2121291246509	SALUNKE SANDHYA DATTATRYA	6	6	13	
2121291246510	SIDDIQUI SHOAIBAHMAD RIYAZAHMAD	4	6	16	
2121291246511	SAYYED SANA MERAJUDDIN	8	6	15	
2121291246512	SARDA PRANJAL SHIVPRASAD	6	6	13	
2121291246513	RUTUJA VIAJAYKUMAR KADAM	8	5	14	
T2021291191020	SHAIKH MOHSIN USMAN	7	7	12	

Enrollment Number	Full Name	Continuous Assessment 2 Marks / 10	Continuous Assessment 1 Marks / 10	Mid Exam Marks / 20	End Exam Marks / 60
		Template Upload	Template Upload	Template Upload	
T2021291191021	SALUNKE NIKITA SIDRAM	4	4	9	
T2021291191027	PRATIK DAGADU ALANGE	4	7	9	
T2021291246001	ABHIJEET DATTATRAYA KULKARNI	7	6	11	
T2021291246002	ADITYA ANANTRAO PATIL	7	5	10	
T2021291246003	ADITYA VIJAY SABNE	4	4	8	
T2021291246004	AISHWARYA SHARNAPPA CHINCHANSURE	4	4	8	
T2021291246005	ANIRUDHA YOGESH KULKAENI	6	4	13	
T2021291246006	ANJALI PRAKASH CHIKATE	4	4	10	
T2021291246007	YERMALWAR ANKITA SANJAY	7	5	10	
T2021291246008	ANURADHA SANJEEVKUMAR BADE	6	4	14	
T2021291246009	SYED BILAL ALI SYED ZAFAR ALI	7	5	12	
T2021291246010	DIVYA AVINASH APSINGKAR	7	6	10	
T2021291246012	GOVIND DASHARATH CHAVAN	5	6	13	
T2021291246013	VATANEVAKIL KIRTI KISHOR	6	4	11	
T2021291246014	PATIL KISHOR RATAN	5	4	8	
T2021291246015	GHAGARE KRUSHNAKANT LAXMAN	6	4	8	
T2021291246016	CHINCHOLE MAHESH VINOD	4	4	8	
T2021291246017	NAGESH VANRAJ BHAT	7	5	14	
T2021291246018	BIRADAR OM LAXMIKANT	6	5	8	
T2021291246019	OMKAR GOVINDRAO JAMADAR	7	4	15	
T2021291246020	OMKAR BIRADAR S/O SOPANRAO BIRADAR	6	5	14	
T2021291246021	SATPUTE PALLAVI SUDHAKAR	5	4	8	
T2021291246022	POOJA MAKARAND BAWANE	7	5	11	

Enrollment Number	Full Name	Continuous Assessment 2 Marks / 10	Continuous Assessment 1 Marks / 10	Mid Exam Marks / 20	End Exam Marks / 60
		Template Upload	Template Upload	Template Upload	
T2021291246023	PRANJALI VYANKAT KASTURE	4	5	8	
T2021291246024	MULGE PRATHAMESH RAGHUNATH	6	6	9	
T2021291246025	LOMATE PRATIK NANASAHEB	4	4	8	
T2021291246026	PRATIKASHA DATTATRAY GAIKWAD	5	5	8	
T2021291246027	PRATIKSHA SURYAKANT SURYAWANSHI	4	4	8	
T2021291246028	PRITAM TANAJI PATIL	6	4	11	
T2021291246029	GHOTALE PRIYA BHARAT	4	4	10	
T2021291246030	RADHIKA LAXMIKANT MOHOLKAR	4	4	9	
T2021291246031	RODGE ROHINI RAMANKANT	4	4	8	
T2021291246032	DAREKAR RUSHIKESH SHIVSHANKAR	6	5	17	
T2021291246033	RUTUJA ANIL THORMOTE	6	8	10	
T2021291246034	KULKARNI SAI MOHAN	5	5	8	
T2021291246035	JADHAV SAKSHI GOKARN	5	5	8	
T2021291246036	HARKARE SANGMESHWAR MALLIKARJUN	6	5	12	
T2021291246037	SANKET SHARAD KARANJKAR	6	4	8	
T2021291246038	SANTOSH BHARAT SHINDE	6	5	8	
T2021291246039	SAYYAD ZUMAN AZIM SAYYAD NAIM	5	5	10	
T2021291246040	SALUNKE SHARAD SURESH	6	6	9	
T2021291246041	SHWETA BHALCHANDRA GADDIME	6	4	11	
T2021291246042	SIDDHARTH NARESH MOTWANI	7	6	12	
T2021291246043	SOHAN SANTOSH JAMDADE	7	4	10	
T2021291246044	KASLE SUMIT SANDIPAN	4	6	8	

Enrollment Number	Full Name	Continuous Assessment 2 Marks / 10	Continuous Assessment 1 Marks / 10	Mid Exam Marks / 20	End Exam Marks / 60
		Template Upload	Template Upload	Template Upload	
T2021291246045	SURAJ SUSHANT GHATKE	4	4	8	
T2021291246046	KUCHAME SUSHILKUMAR GOVIND	5	4	9	
T2021291246047	DESHMUKH SUYOG PARMANAND	5	4	8	
T2021291246048	SWARANJALI SUBHASH PATIL	7	5	11	
T2021291246049	TANAYA RAMESHWAR WAGHMARE	4	4	8	
T2021291246050	TRUPTI GAJENDRA KANADE	7	7	11	
T2021291246051	VAIBHAV PRAKASH TONDARE	7	4	14	
T2021291246052	PAWAR VAIBHAV DNYANOBA	4	4	8	
T2021291246053	DHUMAL VAISHNAVI VAGHAMBAR	4	5	8	
T2021291246054	GATTANI VINAY NANDKISHOR	4	5	9	
T2021291246055	VRASHALI BANDU GAVALI	6	4	10	
T2021291246056	HARSHAL VIPINCHANDRA BARURE	5	4	9	
T2021291246057	MAYURI UMESH BHOSALE	4	4	10	
T2021291298019	LOBHE VIJAYSINGH BHAUSAHEB	7	5	9	
T2021291376003	ANUJA SANGRAM BELURE	4	8	10	
T2021291612006	GUNDILE NARAYAN TATERAO	6	4	11	
T2021291612010	SUMIT NANDKISHOR SOMASE	6	8	11	

Subject Teacher

HOD

(Name and Signature)

(Name and Signature)

Principal/Director

(Name and Signature)



Mahatma Basweshwar Education Society's
M.S. Bidve Engineering College, Latur
Department of Computer Science and Engineering



TimeTable
CA-I Examination Academic Year 2022-23 Part-I

Date	Time	SY	TY	B.Tech
7/10/2022	11.30 AM To 12.00 AM	DS	TOC	
	1.30 PM To 2.00 PM	DM	EL-II(NM)	
	3.30PM To 4.00 PM	CA&O	DBMS	
8/10/2022	11.30 AM To 12.00 AM	EL-I(OOP IN JAVA)	SE	EL-IX CLOUD COMP.
	1.30 PM To 2.00 PM	EM-III	EL-III(BC)	EL-VIII(BIG DATA)
	3.30PM To 4.00 PM			EL-X COMP. GRAPHICS

H.O.D.

Department Of Computer Science and Engineering

H.O.D.

Computer Sci. Engineering
M.S. Bidve Engg College, Latur

M. S. Bidve Engineering College, Latur
DBATU MSE

Class: **SY (IT)** Subject: **EL-I** Date **20-05-22** Time :**11:30am – 12.30pm**

Note: - 1) Solve any four questions

2) Each question carries five marks.

1. What does **HTTP** stand for? Explain http with its **characteristics**.
2. Explain **web server** and how it works.
3. Create a class selector named "**intro**". Add a color property with the value "**blue**" inside the "**intro**" class, add video with **height** and **width** with **auto play** attributes.
4. By using HTML **id** attribute make the Heading (H1) element **red**, background color **light blue**, padding of **40px** and **left** align.
5. Design web page with scrolling text from **left to right**, width **60%**, height **100px** with scroll amount **10**.

M. S. Bidve Engineering College, Latur
DBATU MSE

Class: **SY (IT)** Subject: **EL-I** Date **20-05-22** Time :**11:30am – 12.30pm**

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Mahatma Basweshwar Education Society, Latur

M.S.Bidve Engg.College, Latur

Result Analysis Report

Class: S.Y.(CSE)

Year:2022-23(Summer 2023)

SEM-IV

Sr.No	Faculty	Subject	Result Published	Passed	Failed	Passed(%)	Sign of Faculty
1	Prof.M.P.Bidve	D&AA	76	62	14	82	<i>Prof.M.P.Bidve</i>
2	Prof.S.S.Gujar	OS	76	59	17	78	
3	Prof.N.R.Vyawahare	BHR	76	56	20	74	
4	Prof.S.G.Choudhary	PT&RP	76	44	32	58	
5	Prof.R.B.Late & Prof.S.N.Nagure	DLD&M	76	72	4	95	<i>Prof.S.N.Nagure</i>

D&AA- Design & Analysis of Algorithms, OS- Operating Systems, BHR- Basic Human Rights
PT&RP- Probability Theory and Random Processes, DLD&M- Digital Logic Design & Microprocessors

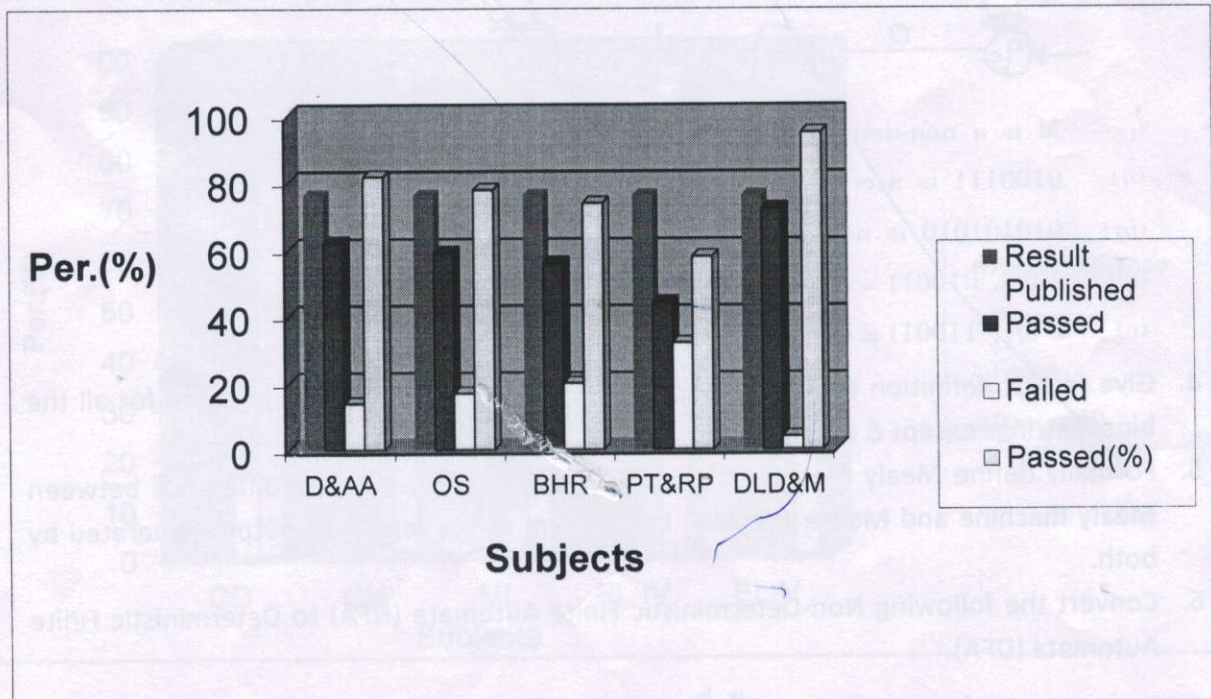
1.Total no.of Results Published = 76

2.Total no. of Students Promoted = 63

(FCWD = 22, FC= 16, ATKT= 25)

PASSING PERCENTAGE OF STUDENTS = $63/76 = 82.89\%$

Graphical Representation



H.O.D.
CSE Dept.

Principal
Principal
M.S.Bidve Engg.College,Latur



Mahatma Basweshwar Education Society, Latur

M.S.Bidve Engg.College, Latur

Result Analysis Report

Class: T.Y.(CSE)

Year:2022-23(Summer 2023)

SEM-VI

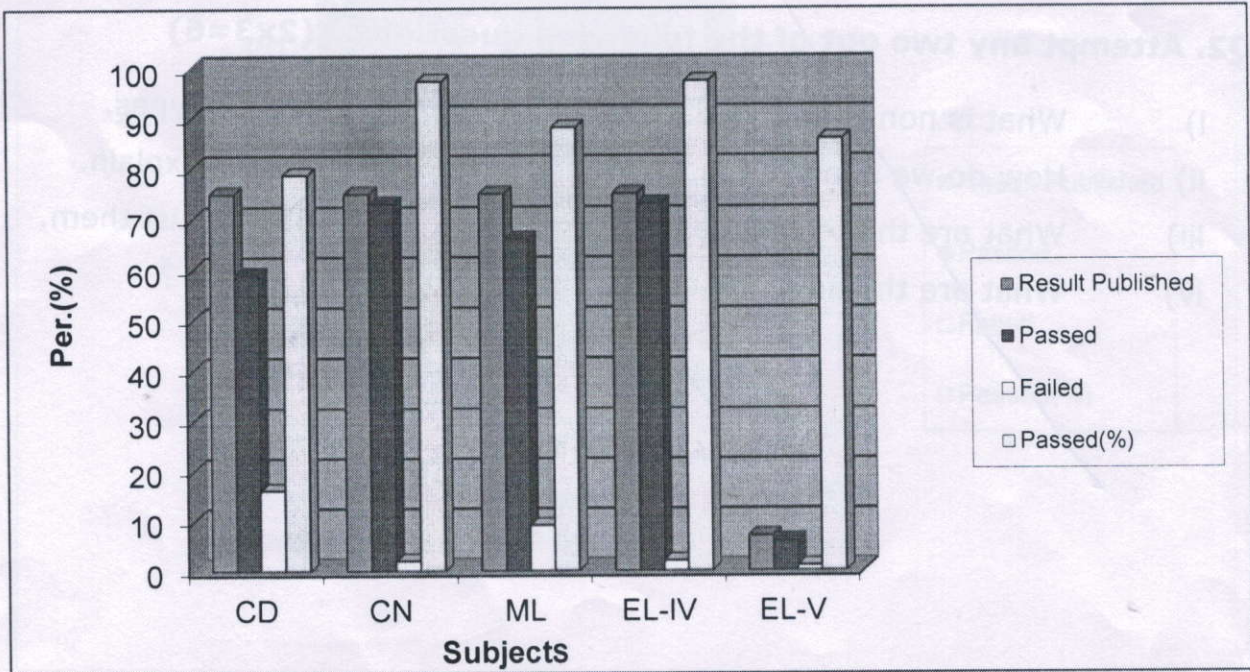
Sr.No	Faculty	Subject	Result Published	Passed	Failed	Passed(%)	Sign of Faculty
1	Prof.N.G.Dharashive	CD	75	59	16	79	
2	Prof.N.J.Pathan	CN	75	73	2	97	
3	Prof.H.P.Shah	ML	75	66	9	88	
4	Prof.R.B.Late	EL-IV	75	73	2	97	
5	Prof.P.J.Kokane	EL-V	75	74	1	99	

CD-Compiler Design, CN-Computer Networks, EL-IV(Internet of Things), EL-V- Consumer Behaviour

- 1.Total no.of Students Result Declared = 75
2. Total no. of Students Promoted = 71
(FCWD=46, FC=4, ATKT= 21)

PASSING PERCENTAGE OF STUDENTS = $71/75 = 94.66\%$

Graphical Representation



H.O.D.
CSE Dept.

Principal
M.S.Bidve Engg.College, Latur



Mahatma Basweshwar Education Society, Latur
M.S.Bidve Engg.College, Latur
Result Analysis Report

Class: B.Tech(CSE)

Year:2022-23(Summer 2023)

SEM-VIII

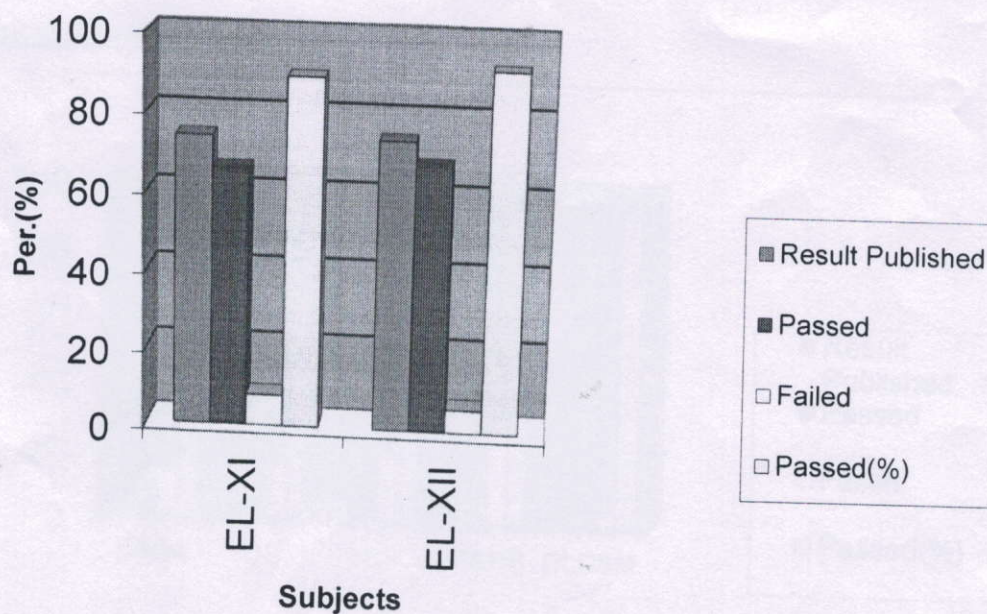
Sr.No	Faculty	Subject	Result Published	Passed	Failed	Passed(%)	Sign of Faculty
1	Prof.N.J.Pathan & Prof.S.S.Gujar	EL-XI	74	66	8	89	<i>[Signature]</i>
2	Prof.S.N.Nagure	EL-XII	74	68	6	92	<i>[Signature]</i>

EL-XI- Social Networks, EL-XII- Cryptography and Network Security

1.Total no.of Students Result Published	=	74
2.Total no.of Students Passed	=	61
3. Total no. of Student Passed with FCWD	=	47

PASSING PERCENTAGE OF STUDENTS = $61/74 = 82.43 \%$

Graphical Representation



[Signature]
H.O.D.
CSE Dept.

[Signature]
Principal
M.S.Bidve Engg.College, Latur



M.S.BIDVE ENGINEERING COLLEGE,LATUR

MID SEM EXAM



Course: B. Tech in Computer Science Engineering

Sem: III

Subject Name: Computer Architecture and Organization

Subject Code: BTCOC304

Max Marks: 20

Date:- 12/11/2022

Time:- 11.30 to 12.30 PM

Marks

Q. 1 Solve Any One of the following.

4*5

- 1) Explain CISC and RISC architecture with neat diagram.
- 2) Explain different addressing modes with neat diagram and example in detail.
- 3) Convert (200.225) in IEEE 754 with single and double precision format floating point representation.
- 4) Explain instruction cycle state diagram with interrupts.
- 5) Explain flowchart for booth's algorithm for twos complement multiplication and perform multiplication with example (5*3).
- 6) Explain flowchart for unsigned binary division and perform division with example (6/3).
- 7) Perform floating point numbers arithmetic operations. $X = 0.4 \times 10^2$ $Y = 0.3 \times 10^3$

**UNIT TEST ANSWER BOOK** No.Name of the student : Nadgire Shivani Santosh Roll No. : 55Class : EC 2nd year Subject : PT & RP**Details of unit test**

	Unit Test - I	Unit Test - II	Unit Test - III	Final Marks
Date of Unit Test	—	—	—	
Marks Obtained	10/10	17/20	9½ = 10/10	
Sign. of Faculty with date	Half	Half	Half	
Sign. of Student with date	Kavani	Kavani		

Start answer from here.

Q.1)

→

Sample space

$$S = \{1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50\}$$
 $\therefore$ no. of sample space $n(S) = 50$

Event A : Number is square of drawing.

$$A = \{1, 4, 9, 16, 25, 36, 49\}$$
 $\therefore n(A) = 7$

M. B. EDUCATION SOCIETY'S
M. S. BIDVE ENGINEERING COLLEGE, LATUR
 (Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere)
 Part - I / II
 Academic Year -

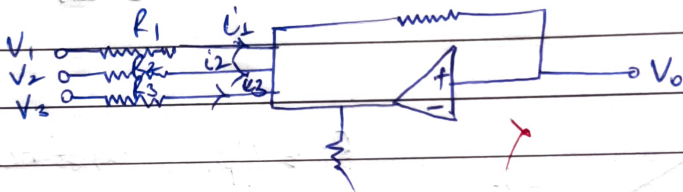
UNIT TEST ANSWER BOOK No.

Name of the student: Aranti Suryawarshi Roll No.: 24
 TY (EC) Subject: Analog circuit

of unit test

	Unit Test - I	Unit Test - II	Unit Test - III	Final Marks
of Unit Test				
Obtained	$\frac{06}{10}$	$\frac{05}{20}$		
of Faculty late	$\frac{01}{6/10/23}$	$\frac{01}{27/10/23}$		
of Student late				

Start answer from here.



$$V_i + V_2 + V_3 = V_f$$

$$V_o = -(V_1 + 2V_2 + V_3) \quad \text{--- ①}$$

$$V_o = -\frac{R_F}{R_1} V_1 - \frac{R_F}{R_2} V_2 - \frac{R_F}{R_3} V_3 \quad \text{--- ②}$$

from comparing eqⁿ ① and ②

$$\frac{R_F}{R_1} = 1 ; \frac{R_F}{R_2} = 2 ; \frac{R_F}{R_3} = 1$$

Let $R_F = 10k\Omega$

$$\frac{R_F}{1} = R_1 ; \frac{R_F}{2} = R_2 ; \frac{R_F}{1} = R_3$$